



#91 Prior Art
T. McBeath
PATENT 4/23/04

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Paul A. Martin et al.

Title: CONCURRENT SHARED OBJECT IMPLEMENTED USING A LINKED LIST AMORTIZED NODE ALLOCATION

Application No.: 09/837,669

Filed: April 18, 2000

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Examiner: Lewis Alexander
Bullock Jr.

Group Art Unit: 2126

APR 20 2004

Technology Center 2100

Atty. Docket No.: 004-4665-1

April 13, 2004

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INFORMATION DISCLOSURE STATEMENT
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- ☒ Form(s) PTO-1449
☒ Other: Copies of 9 references

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

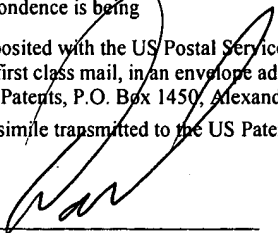
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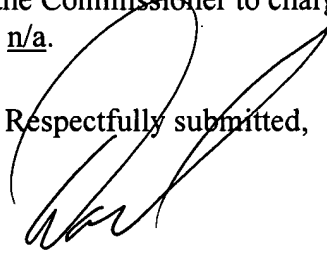
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The undersigned believes that this Information Disclosure Statement is being filed before the mailing date of (i) a final action under § 1.113, (ii) a notice of allowance under § 1.311 or (iii) an action that otherwise closes prosecution. Accordingly, this Information Disclosure Statement is accompanied by the fee set forth in § 1.17(p) as follows:

- ☒ A check in the amount of \$180.00 is enclosed herewith.
- ☐ The undersigned hereby authorizes the Commissioner to charge the fee set forth in § 1.17(p) to Deposit Account No. n/a.

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 _____ David W. O'Brien	<u>13-Apr-04</u> _____ Date

Respectfully submitted,


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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-4665-1
		Application No.: 09/837,669
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		Applicant(s): Paul A. Martin et al.
		Filing Date: April 18, 2000
		Group Art Unit: 2126
		Date Submitted: April 13, 2004
NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	1.	Blumofe, Robert D. et al., "Verification of a Concurrent Deque Implementation", University of Texas at Austin, Department of Computer Sciences, Technical Report TR99-11, June 1999.
	2.	Farook, Mohammad et al., "Managing Long Linked Lists Using Lock Free Techniques", University of Manitoba, Canada, 1998.
	3.	Michael, Maged M. et al., "Simple, Fast, and Practical Non-Blocking and Blocking Concurrent Queue Algorithms", University of Rochester, Department of Computer Science, 1996.
	4.	Prakash, Sundeep et al., "Non-Blocking Algorithms for Concurrent Data Structures", University of Florida, July 1, 1991.
	5.	Prakash, Sundeep et al., "A Nonblocking Algorithm for Shared Queues Using Compare-and-Swap", IEEE Transactions on Computers, Vol. 43, No. 5, May 1994.
	6.	Shann, Chien-Hua et al., "A Practical Nonblocking Queue Algorithm Using Compare-and-Swap", IEEE, July 2000.
	7.	Turek, John et al., "Locking Without Blocking: Making Lock Based Concurrent Data Structure Algorithms Nonblocking", ACM, 1992.
	8.	Valois, John D., "Lock-Free Linked Lists Using Compare-and-Swap", ACM, 1995.
	9.	IBM Technical Disclosure Bulletin, "Conditional Multiple Store Instruction", February 1, 1980.
Examiner		Date Considered
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		

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